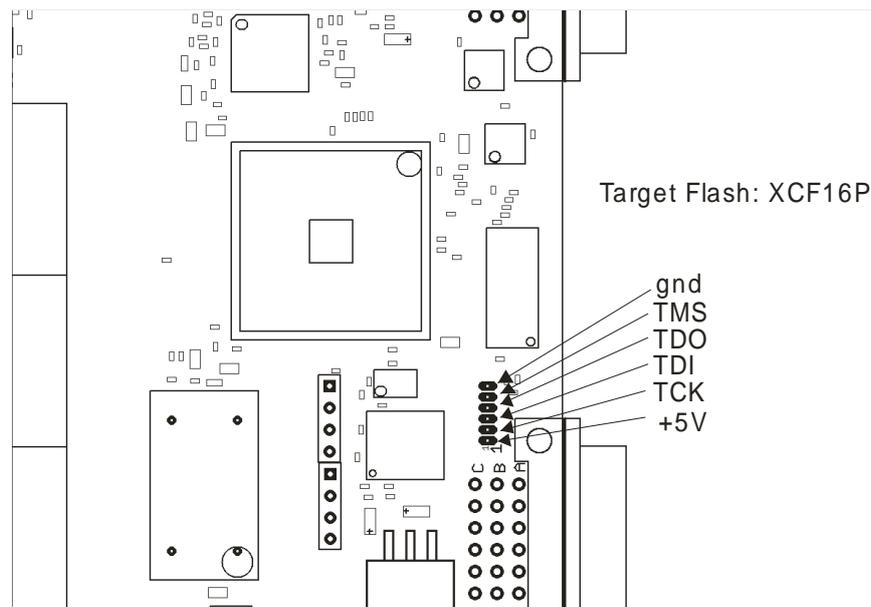**Programming of mesytec VME modules**

Download Xilinx Lab Tools - 14.7 Utilities . It is necessary to regiser, but no license is required.
http://www.xilinx.com/support/download/index.html/content/xilinx/en/downloadNav/design-tools.html
Install programmer "impact"

Programming connector MTDC-32, MQDC-32

Connect mesytec programming cable to your parallel port. The target VME module has to be powered. Connect the target connector to the target module. When using the 6 pin connector be careful with polarity.
Start Wizard (third icon from left at the top), mark first point "Configure devices using..." press ok. The Xilinx prom should now be found.
Follow the wizard and load the mesytec configuration (=programming-) file (xxxxx.mcs).
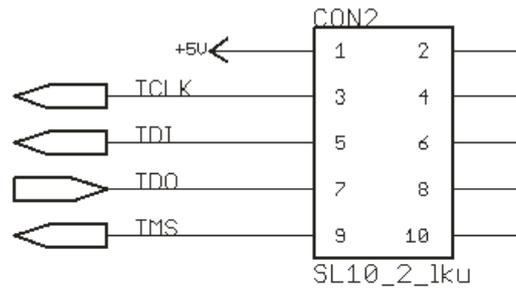Now a window with configuration options opens. See following sections for settings.

MTDC: Programmer setting: "Prom is configuration master",
                         "Internal clock 40MHz"

MQDC: Programmer setting: default (FPGA is programming master)
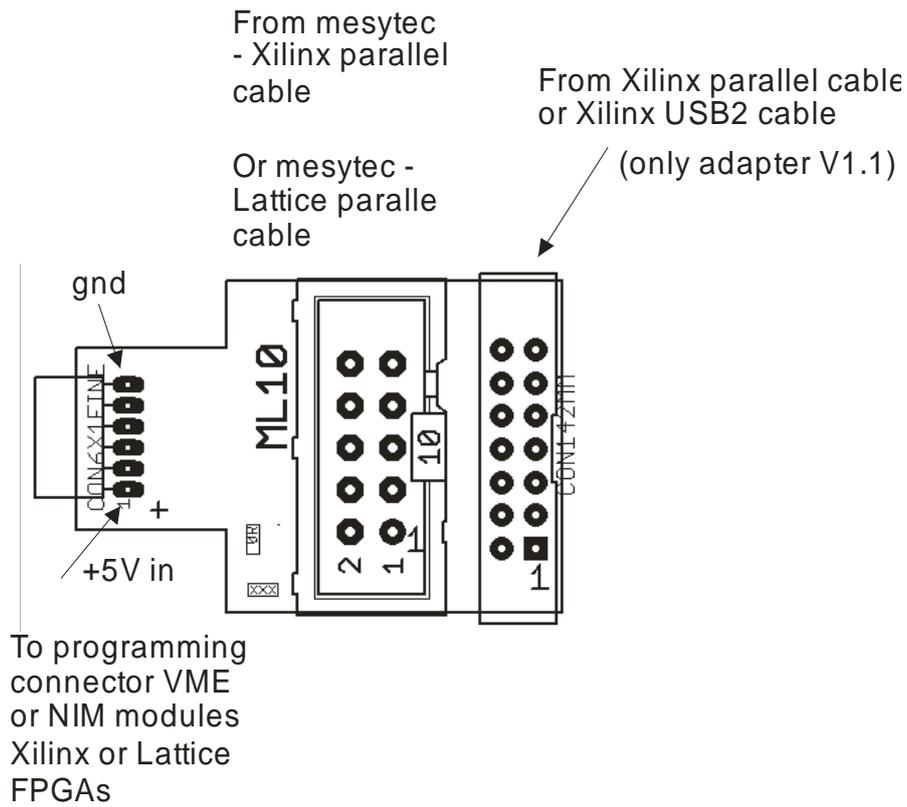
   MQDC: different firmware for Hardware revision V13 and V14 !!

MADC: Programmer setting: default

Only upgrade for MADC V2x available (delivered from 2009)

Programming connector of MADC:

Mesytec Adapter for Xilinx programmers and mesytec cables:

From mesytec
- Xilinx parallel
cable

Or mesytec -
Lattice paralle
cable

From Xilinx parallel cable
or Xilinx USB2 cable

(only adapter V1.1)

gnd

+5V in

To programming
connector VME
or NIM modules
Xilinx or Lattice
FPGAs

Connectors on mesytec adapter

Same order as on PCB:

mesytec board
connector

mesytec cable
connector

Xilinx connector